

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	(@ad<"20040329" @rlad<"20040329") and (socket port) same (microprocessor adj bus) and ((network tcp\$5 ethernet) same (socket port)) and (input adj channel) and (output adj channel)	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17
L3	2	(@ad<"20040329" @rlad<"20040329") and (socket port) same (microprocessor adj bus) and ((network tcp\$5 ethernet) same (socket port)) and ((input inbound) adj (link channel)) and ((output outbound) adj (link channel)) not L2	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17
L4	233	(@ad<"20040329" @rlad<"20040329") and (socket port) same (microprocessor) and ((network tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (socket port)) and ((input inbound) adj (link channel)) and ((output outbound) adj (link channel)) not (L2 L3)	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17
L5	167	(@ad<"20040329" @rlad<"20040329") and (socket port network internet tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (microprocessor micro- processor) and ((network internet tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (socket port)) and ((input inbound) adj (link channel)) and ((output outbound) adj (link	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17

		channel)) not (L2 L3 L4)				
L6	16	(@ad<"20040329" @rlad<"20040329") and (socket port network internet tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (microprocessor micro- controller micro-processor micro-CPU micro-computer) and ((network internet tcp \$5 ethernet wireless mobil \$3 handheld roam\$4) same (socket port)) and ((input inbound) adj (link channel)) and ((output outbound) adj (link channel)) not (L2 L3 L4 L5)	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17
L7	634	(@ad<"20040329" @rlad<"20040329") and (socket port network internet tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (microprocessor micro- controller micro-processor micro-CPU micro-computer) and ((network internet tcp \$5 ethernet wireless mobil \$3 handheld roam\$4) same (socket port)) and ((input inbound transmit\$5 send \$5) adj (link channel)) and ((output outbound receiv\$5 respon\$5) adj (link channel)) not (L2 L3 L4 L5 L6)	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17
L8	126	L7 and ("709" "710").clas.	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17

L9	21	(@ad<"20040329" @rlad<"20040329") and (socket port network internet tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (microprocessor micro- controller micro-processor micro-CPU micro-computer) and ((network internet tcp \$5 ethernet wireless mobil \$3 handheld roam\$4) same (socket port)) and ((input inbound transmit\$5 send \$5) adj (link channel)) and ((output outbound receiv\$5 respon\$5) adj (link channel)) and "712". clas.	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17
L10	61	(@ad<"20040329" @rlad<"20040329") and (socket port network internet tcp\$5 ethernet wireless mobil\$3 handheld roam\$4) same (microprocessor micro- controller micro-processor micro-CPU micro-computer) and ((network internet tcp \$5 ethernet wireless mobil \$3 handheld roam\$4) same (socket port)) and ((input inbound transmit\$5 send \$5) adj (link channel)) and ((output outbound receiv\$5 respon\$5) adj (link channel)) and ("709" "710" "711" "712").clas. not (L9 L8)	US- PGPUB; USPAT	OR	ON	2008/04/21 09:17

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Manager interface for Port to Microprocessor Bus.w sp